Features	CPU CR20s	CPU CR30s	CPU CR40s, CPU CR40	CPU CR60s, CPU CR60
PID loops	8	8	8	8
Real-time clock with 7-day back-up	No	No	No	No

[1] You can configure areas of V memory, M memory, C memory (current values), and portions of T memory (current values on retentive timers) to be retentive, up to the specified maximum amount.

Table 1-3 Standard expandable CPUs

Features		CPU SR20, CPU ST20	CPU SR30, CPU ST30	CPU SR40, CPU ST40	CPU SR60, CPU ST60
Dimensions: W x H x D (mm)		90 x 100 x 81	110 x 100 x 81	125 x 100 x 81	175 x 100 x 81
User memory	Program	12 Kbytes	18 Kbytes	24 Kbytes	30 Kbytes
	User data	12 Kbytes	16 Kbytes	20 Kbytes	24 Kbytes
	Retentive	12 Kbytes max.[1]	12 Kbytes max.[1]	12 Kbytes max.[1]	12 Kbytes max.[1]
On-board digital I/O	• Inputs • Outputs	• 12 DI • 8 DQ	• 18 DI • 12 DQ	• 24 DI • 16 DQ	• 36 DI • 24 DQ
Expansion modules		6 max.	6 max.	6 max.	6 max.
Signal board		1	1	1	1
High-speed counters (6 total)	Single phase	4 at 200 kHz 2 at 30 kHz	5 at 200 kHz 1 at 30 kHz	4 at 200 kHz 2 at 30 kHz	4 at 200 kHz 2 at 30 kHz
	A/B phase	2 at 100 kHz 2 at 20 kHz	3 at 100 kHz 1 at 20 kHz	2 at 100 kHz 2 at 20 kHz	2 at 100 kHz 2 at 20 kHz
Pulse outputs [2]		2 at 100 kHz	3 at 100 kHz	3 at 100 kHz	4 at 100 kHz
PID loops		16	16	16	16
Real-time clock with 7-day back-up		Yes	Yes	Yes	Yes

[1] You can configure areas of V memory, M memory, C memory (current values), and portions of T memory (current values on retentive timers) to be retentive, up to the specified maximum amount.

[2] The specified maximum pulse frequency is possible only for CPU models with transistor outputs. Pulse output operation is not recommended for CPU models with relay outputs.

Refer to the technical specifications (Page 839) for the power requirements of the CPU and the expansion modules. Use the worksheets in Appendix B, Calculating a power budget (Page 946) to calculate your power budget (Page 943).

CPU model	Article number
CPU ST40, DC/DC/DC	6ES7288-1ST40-0AA1
CPU SR60, AC/DC/Relay	6ES7288-1SR60-0AA1
CPU ST60, DC/DC/DC	6ES7288-1ST60-0AA1

Note

These new models cannot be downgraded to V2.5 or any previous version.

Note

Any previous SR or ST model with an article number ending with 0AA0 cannot be upgraded to V2.6.

STEP 7-Micro/WIN SMART V2.6 release provides the following new features:

Web server

STEP 7-Micro/WIN SMART V2.6 and the S7-200 SMART V2.6 CPU firmware add Web Server functions.

Web server wizard (Page 183)

The Web server wizard is used to configure Web server functions.

Certificate management wizard (Page 190)

The certificate management wizard is used to manage, download and upload certificates for the Web server.

Web pages (Page 201)

Web pages provide the function to view and modify the PLC data through browsers.

1.2.4 What's new in S7-200 SMART V2.5?

Only the following CPU models with firmware V2.5 or later versions support the new features described in this chapter:

Table 1-7 CPU models affected by firmware V2.5

CPU model	Article number
CPU SR20, AC/DC/Relay	6ES7288-1SR20-0AA0
CPU ST20, DC/DC/DC	6ES7288-1ST20-0AA0
CPU SR30, AC/DC/Relay	6ES7288-1SR30-0AA0
CPU ST30, DC/DC/DC	6ES7288-1ST30-0AA0
CPU SR40, AC/DC/Relay	6ES7288-1SR40-0AA0
CPU ST40, DC/DC/DC	6ES7288-1ST40-0AA0
CPU SR60, AC/DC/Relay	6ES7288-1SR60-0AA0
CPU ST60, DC/DC/DC	6ES7288-1ST60-0AA0

A.2.4 CPU ST60, CPU SR60, CPU CR60s, and CPU CR60

A.2.4.1 General specifications and features

Table A-56 General specifications

Technical data	CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay	CPU CR60s AC/DC/Relay	CPU CR60 AC/DC/Relay
Article number	6ES7288-1ST60-0AA1	6ES7288-1SR60-0AA1	6ES7288-1CR60-0AA1	6ES7288-1CR60-0AA0
Dimensions W x H x D (mm)	175 x 100 x 81	175 x 100 x 81	175 x 100 x 81	175 x 100 x 81
Weight	528.2 grams	611.5 grams	605 grams	621.9 grams
Power dissipation	20 W	25 W	10 W	10 W
Current available (EM bus)	1400 mA max. (5 V DC)	1400 mA max. (5 V DC)	Not available	Not available
Current available (24 V DC)	300 mA max. (sensor power)	300 mA max. (sensor power)	300 mA max. (sensor power)	300 mA max. (sensor power)
Digital input current consumption (24 V DC)	4 mA/input used	4 mA/input used	4 mA/input used	4 mA/input used

Table A-57 CPU features

Technical data		CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay	CPU CR60s AC/DC/Relay	CPU CR60 AC/DC/Relay
User memory	Program	30 Kbytes	30 Kbytes	12 Kbytes	
	User data (V)	24 Kbytes	24 Kbytes	8 Kbytes	
	Retentive	12 Kbytes max.[1]	12 Kbytes max.[1]	2 Kbytes max.[1]	
On-board digital I/O		36 inputs/24 outputs	36 inputs/24 outputs	36 inputs/24 outputs	
Process image		256 bits of inputs (I) / 256 bits of outputs (Q)	256 bits of inputs (I) / 256 bits of outputs (Q)	256 bits of inputs (I) / 256 bits of outputs (Q)	
Analog image		56 words of inputs (AI) / 56 words of outputs (AQ)	56 words of inputs (AI) / 56 words of outputs (AQ)	Not available	
Bit memory (M)		256 bits	256 bits	256 bits	
Temporary (local) memory (L)		64 bytes in the main program and 64 bytes in each subroutine and interrupt routine 60 bytes when programming in LAD or FBD (STEP 7-Micro/WIN reserves 4 bytes)	64 bytes in the main program and 64 bytes in each subroutine and interrupt routine 60 bytes when programming in LAD or FBD (STEP 7-Micro/WIN reserves 4 bytes)	64 bytes in the main program and 64 bytes in each subroutine and interrupt routine 60 bytes when programming in LAD or FBD (STEP 7-Micro/WIN reserves 4 bytes)	
Sequential control relays (S)		256 bits	256 bits	256 bits	
Expansion modules expansion		6 max.	6 max.	Not available	

Technical data		**CPU ST60 DC/DC/DC**	**CPU SR60 AC/DC/Relay**	**CPU CR60s AC/DC/Relay**	**CPU CR60 AC/DC/Relay**
Signal board expansion		1 max.	1 max.	Not available	
High-speed counters	Total	6	6	4	
	Single phase	4 at 200 kHz 2 at 30 kHz	4 at 200 kHz 2 at 30 kHz	4 at 100 kHz	
	A/B phase	2 at 100 kHz 2 at 20 kHz	2 at 100 kHz 2 at 20 kHz	2 at 50 kHz	
Pulse outputs [2]		3 at 100 kHz	3 at 100 kHz	Not available	
Pulse catch inputs		14	14	Not available	
Cyclic interrupts		2 at 1 ms resolution	2 at 1 ms resolution	2 at 1 ms resolution	
Edge interrupts		4 rising and 4 falling (6 and 6 with optional signal board)	4 rising and 4 falling (6 and 6 with optional signal board)	4 rising and 4 falling	
Memory card		microSDHC card (optional)	microSDHC card (optional)	Not available	
Real time clock accuracy		120 seconds/month	120 seconds/month	Not available	
Real time clock retention time		7 days typ./6 days min. at 25 °C	7 days typ./6 days min. at 25 °C	Not available	

[1] You can configure areas of V memory, M memory, C memory (current values) and portions of T memory (current values on retentive timers) to be retentive, up to the specified maximum amount.

[2] The specified maximum pulse frequency is possible only for CPU models with transistor outputs. Pulse output operation is not recommended for CPU models with relay outputs.

Table A-58 PROFINET features

Description	**CPU ST60 DC/DC/DC**	**CPU SR60 AC/DC/Relay**
Support Controller function for PROFINET communication	Yes	
Support I-Device function for PROFINET communication	Yes	
Maximum number of PROFINET device	8	
Device number of PROFINET device	1 to 8	
Maximum input size of each PROFINET device	128 Bytes	
Maximum output size of each PROFINET device	128 Bytes	
Maximum input size of PROFINET I-Device	128 Bytes	
Maximum output size of PROFINET I-Device	128 Bytes	
Maximum number of modules	64	
Minimum cyclic update time of PROFINET device	The minimum value of the update time also depends on the communication component set for PROFINET, on the number of PROFINET devices, and the quantity of configured user data.	
CPU address range of PROFINET process image input register	I128.0 to I1279.7	
CPU address range of PROFINET process image output register	Q128.0 to Q1279.7	

Description	CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay
CPU address of PROFINET process image input and output register for device #1	I128.0 to I255.7 Q128.0 to Q255.7	
CPU address of PROFINET process image input and output register for device #2	I256.0 to I383.7 Q256.0 to Q383.7	
CPU address of PROFINET process image input and output register for device #3	I384.0 to I511.7 Q384.0 to Q511.7	
CPU address of PROFINET process image input and output register for device #4	I512.0 to I639.7 Q512.0 to Q639.7	
CPU address of PROFINET process image input and output register for device #5	I640.0 to I767.7 Q640.0 to Q767.7	
CPU address of PROFINET process image input and output register for device #6	I768.0 to I895.7 Q768.0 to Q895.7	
CPU address of PROFINET process image input and output register for device #7	I896.0 to I1023.7 Q896.0 to Q1023.7	
CPU address of PROFINET process image input and output register for device #8	I1024.0 to I1151.7 Q1024.0 to Q1151.7	
CPU address of PROFINET process image input and output register for I-Device	I1152.0 to I1279.7 Q1152.0 to Q1279.7	

Table A-59 Performance

Type of instruction	Execution speed
Boolean	150 ns instruction
Move Word	1.2 µs/instruction
Real math	3.6 µs/instruction

Table A-60 User program elements supported

Element		Description
POUs	Type/quantity	Main program: 1 Subroutines: 128 (0 to 127) Interrupt routines: 128 (0 to 127)
	Nesting depth	From main program: 8 subroutine levels From interrupt routine: 4 subroutine levels
Accumulators	Quantity	4
Timers	Type/quantity	Non-retentive (TON, TOF): 192 Retentive (TONR): 64
Counters	Quantity	256

Table A-61 Communication

Technical data	CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay	CPU CR60s AC/DC/ Relay	CPU CR60 AC/DC/Relay
Number of ports	PROFINET (LAN): 1 Serial ports: 1 (RS485) Add-on serial ports: 1 (with optional RS232/485 signal board)	PROFINET (LAN): 1 Serial ports: 1 (RS485) Add-on serial ports: 1 (with optional RS232/485 signal board)	PROFINET (LAN): 0 Serial ports: 1 (RS485) Add-on serial ports: 0	PROFINET (LAN): 1 Serial ports: 1 (RS485) Add-on serial ports: 0
HMI device	PROFINET (LAN): 8 connections Serial ports: 4 connections per port	PROFINET (LAN): 8 connections Serial ports: 4 connections per port	PROFINET (LAN): Not available Serial ports: 4 connections per port	PROFINET (LAN): 8 connections Serial ports: 4 connections per port
Programming device (PG)	PROFINET (LAN): 1 connection Serial ports: 1 connection	PROFINET (LAN): 1 connection Serial ports: 1 connection	PROFINET (LAN): Not available Serial ports: 1 connection	PROFINET (LAN): 1 connection Serial ports: 1 connection
CPUs (PUT/ GET)	PROFINET (LAN): 8 client and 8 server connections	PROFINET (LAN): 8 client and 8 server connections	PROFINET (LAN): Not available	PROFINET (LAN): 8 client and 8 server connections
Open user communication	PROFINET (LAN): 8 active and 8 passive connections	PROFINET (LAN): 8 active and 8 passive connections	PROFINET (LAN): Not available	PROFINET (LAN): 8 active and 8 passive connections
Data rates	PROFINET (LAN): 10/100 Mb/s RS485 system protocols: 9600, 19200, and 187500 b/s RS485 freeport: 1200 to 115200 b/s	PROFINET (LAN): 10/100 Mb/s RS485 system protocols: 9600, 19200, and 187500 b/s RS485 freeport: 1200 to 115200 b/s	PROFINET (LAN): Not available RS485 system protocols: 9600, 19200, and 187500 b/s RS485 freeport: 1200 to 115200 b/s	PROFINET (LAN): 10/100 Mb/s RS485 system protocols: 9600, 19200, and 187500 b/s RS485 freeport: 1200 to 115200 b/s
Isolation (external signal to PLC logic)	PROFINET (LAN): Transformer isolated, 1500 V AC RS485: RS485 signal to chassis ground, 500 V AC/ 707 V DC; RS485 signal to CPU logic common, 500 V AC/ 707V DC;	PROFINET (LAN): Transformer isolated, 1500 V AC RS485: RS485 signal to chassis ground, 500 V AC/ 707 V DC; RS485 signal to CPU logic common, 500 V AC/ 707V DC;	PROFINET (LAN): Not available RS485: RS485 signal to chassis ground, 500 V AC/ 707 V DC; RS485 signal to CPU logic common, 500 V AC/ 707V DC;	PROFINET (LAN): Transformer isolated, 1500 V AC RS485: RS485 signal to chassis ground, 500 V AC/ 707 V DC; RS485 signal to CPU logic common, 500 V AC/ 707V DC;
Cable type	PROFINET (LAN): CAT5e shielded RS485: PROFIBUS network cable	PROFINET (LAN): CAT5e shielded RS485: PROFIBUS network cable	PROFINET (LAN): Not available RS485: PROFIBUS network cable	PROFINET (LAN): CAT5e shielded RS485: PROFIBUS network cable
PROFINET Communication				
PROFINET controller	Yes	Yes	No	No
PROFINET device	Yes	Yes	No	No
PROFINET controller				
Services				

Technical data	CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay	CPU CR60s AC/DC/ Relay	CPU CR60 AC/DC/Relay
PG/OP communication	Yes	Yes	No	No
S7 routing	Yes	Yes	No	No
Isochronous mode	No	No	No	No
Open IE communication	Yes	Yes	No	No
IRT	No	No	No	No
MRP	No	No	No	No
PROFIenergy	No	No	No	No
Max. number of PROFINET devices that you can connect for RT	8	8	--	--
Max. number of module	64	64	--	--
Update times	The minimum value of the update time also depends on the communication component set for PROFINET, on the number of PROFINET devices, and the quantity of configured user data.	The minimum value of the update time also depends on the communication component set for PROFINET, on the number of PROFINET devices, and the quantity of configured user data.	No	No
With RT				
Send clock of 1 ms	1 ms to 512 ms	1 ms to 512 ms	--	--

Table A-62 Power supply

Technical data	CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay		CPU CR60s AC/DC/Relay CPU CR60 AC/DC/Relay	
Voltage range	20.4 to 28.8 V DC	85 to 264 V AC	77 to 138 V DC	85 to 264 V AC	77 to 138 V DC
Line frequency	--	47 to 63 Hz	--	47 to 63 Hz	--

Technical data		**CPU ST60 DC/DC/DC**	**CPU SR60 AC/DC/Relay**		**CPU CR60s AC/DC/Relay** **CPU CR60 AC/DC/Relay**	
Input current (max. load)	CPU only	220 mA at 24 V DC (without driving 300 mA sensor power) 500 mA at 24 V DC (with driving 300 mA sensor power)	160 mA at 120 V AC (without driving 300 mA sensor power) 280 mA at 120 V AC (with driving 300 mA sensor power) 90 mA at 240 V AC (without driving 300 mA sensor power) 160 mA at 240 V AC (with driving 300 mA sensor power)	150 mA at 110 VDC (with 300 mA power sensor output) 73 mA at 110 VDC (without 300 mA power sensor output)	150 mA at 120 V AC 100 mA at 240 V AC	150 mA at 110 VDC (with 300 mA power sensor output) 73 mA at 110 VDC (without 300 mA power sensor output)
	CPU with all expansion accessories	710 mA at 24 V DC	370 mA at 120 V AC 220 mA at 240 V AC	234 mA at 110 VDC (with 300 mA power sensor output)	Not available	234 mA at 110 VDC (with 300 mA power sensor output)
Hold up time (loss of power)		20 ms at 24 V DC	30 ms at 120 V AC 200 ms at 240 V AC	30 ms at 110 V DC	30 ms at 120 V AC 200 ms at 240 V AC	30 ms at 110 V DC
Inrush current (max.)		11.5 A at 28.8 V DC	16.3 A at 264 V DC		16.3 A at 264 V AC	
Isolation (input power to logic)		None	1500 V AC		1500 V AC	
Ground leakage, AC line to functional earth		--	0.5 mA		0.5 mA	
Internal fuse, not user replaceable		3 A, 250 V, slow blow	3 A, 250 V, slow blow		3 A, 250 V, slow blow	

Table A-63 Sensor power

Technical data	**CPU ST60 DC/DC/DC**	**CPU SR60 AC/DC/Relay**	**CPU CR60s AC/DC/Relay**	**CPU CR60 AC/DC/Relay**
Voltage range	20.4 to 28.8 V DC	20.4 to 28.8 V DC	20.4 to 28.8 V DC	
Output current rating (max.)	300 mA	300 mA	300 mA (short circuit protected)	
Maximum ripple noise (<10 MHz)	< 1 V peak to peak	< 1 V peak to peak	< 1 V peak to peak	
Isolation (CPU logic to sensor power)	Not isolated	Not isolated	Not isolated	

A.2.4.2 Digital inputs and outputs

Table A-64 Digital inputs

Technical data	CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay	CPU CR60s AC/DC/Relay	CPU CR60 AC/DC/Relay
Number of inputs	36	36	36	
Type	Sink/Source (IEC Type 1 sink, except I0.0 to I0.3)	Sink/Source (IEC Type 1 sink)	Sink/Source (IEC Type 1 sink)	
Rated voltage	24 V DC at 4 mA, nominal	24 V DC at 4 mA, nominal	24 V DC at 4 mA, nominal	
Continuous permissible voltage	30 V DC, max.	30 V DC, max.	30 V DC, max.	
Surge voltage	35 V DC for 0.5 sec.	35 V DC for 0.5 sec.	35 V DC for 0.5 sec.	
Logic 1 signal (min.)	I0.0 to I0.3: 4 V DC at 8 mA Other inputs: 15 V DC at 2.5 mA	15 V DC at 2.5 mA	15 V DC at 2.5 mA	
Logic 0 signal (max.)	I0.0 to I0.3: 1 V DC at 1 mA Other inputs: 5 V DC at 1 mA	5 V DC at 1 mA	5 V DC at 1 mA	
Isolation (field side to logic)	500 V AC for 1 minute	500 V AC for 1 minute	500 V AC for 1 minute	
Isolation groups	1	1	1	
Filter times	Individually selectable on each channel (points I0.0 to I1.5): µs: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8 ms: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8	Individually selectable on each channel (points I0.0 to I1.5): µs: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8 ms: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8	Individually selectable on each channel (points I0.0 to I1.5): µs: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8 ms: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8	
	Individually selectable on each channel (points I1.6 and greater): ms: 0, 6.4, 12.8	Individually selectable on each channel (points I1.6 and greater): ms: 0, 6.4, 12.8	Individually selectable on each channel (points I1.6 and greater): ms: 0, 6.4, 12.8	
HSC clock input rates (max.) (Logic 1 Level = 15 to 26 V DC)	Single phase: 4 HSCs at 200 kHz; 2 HSCs at 30 kHz A/B phase: 2 HSCs at 100 kHz; 2 HSCs at 20 kHz	Single phase: 4 HSCs at 200 kHz; 2 HSCs at 30 kHz A/B phase: 2 HSCs at 100 kHz; 2 HSCs at 20 kHz	Single phase: 4 HSCs at 100 kHz A/B phase: 2 HSCs at 50 kHz	
Number of inputs on simultaneously	36	36	36	
Cable length (max.), in meters	I0.0 to I0.3: Shielded (only): • 500 m normal (low-speed) inputs • 50 m HSC (high-speed) inputs	All inputs: Shielded: • 500 m normal inputs • 50 m HSC inputs Unshielded: • 300 m normal inputs	All inputs: Shielded: • 500 m normal inputs • 50 m HSC inputs Unshielded: • 300 m normal inputs	
	All other inputs: Shielded: • 500 m normal inputs Unshielded: • 300 m normal inputs			

Table A-65 Digital outputs

Technical data	CPU ST60 DC/DC/DC	CPU SR60 AC/DC/Relay	CPU CR60s AC/DC/Relay	CPU CR60 AC/DC/Relay
Number of outputs	24	24	24	
Type	Solid state - MOSFET (sourcing)	Relay, dry contact	Relay, dry contact	
Voltage range	20.4 to 28.8 V DC	5 to 30 V DC or 5 to 250 V AC	5 to 30 V DC or 5 to 250 V AC	
Logic 1 signal at max. current	20 V DC min.	--	--	
Logic 0 signal with 10 KΩ load	0.1 V DC max.	--	--	
Rated current per point (max.)	0.5 A	2 A	2 A	
Rated current per common (max.)	6 A	10 A	10 A	
Lamp load	5 W	30 W DC / 200 W AC	30 W DC / 200 W AC	
ON state resistance	0.6 Ω max.	0.2 Ω max. when new	0.2 Ω max. when new	
Leakage current per point	10 µA max.	--	--	
Surge current	8 A for 100 ms max.	7 A with contacts closed	7 A with contacts closed	
Overload protection	No	No	No	
Isolation (field side to logic)	500 V AC for 1 minute	1500 V AC for 1 minute (coil to contact) None (coil to logic)	1500 V AC for 1 minute (coil to contact) None (coil to logic)	
Isolation resistance	--	100 M Ω min. when new	100 M Ω min. when new	
Isolation between open contacts	--	750 V AC for 1 minute	750 V AC for 1 minute	
Isolation groups	3	6	6	
Inductive clamp voltage	L+ minus 48 V DC, 1 W dissipation	--	-	
Switching delay (Qa.0 to Qa.3)	1.0 µs max., off to on 3.0 µs max., on to off	10 ms max.	10 ms max.	
Switching delay (Qa.4 to Qc.7)	50 µs max., off to on 200 µs max., on to off	10 ms max.	10 ms max.	
Maximum relay switching frequency	--	1 Hz	1 Hz	
Lifetime mechanical (no load)	--	10,000,000 open/close cycles	10,000,000 open/close cycles	
Lifetime contacts at rated load	--	100,000 open/close cycles	100,000 open/close cycles	
Output behavior in STOP	Last value or substitute value (default value 0)	Last value or substitute value (default value 0)	Last value or substitute value (default value 0)	
Number of outputs on simultaneously	24	24	24	
Cable length (max.), in meters	Shielded: 500 m Unshielded: 150 m	Shielded: 500 m Unshielded: 150 m	Shielded: 500 m Unshielded: 150 m	

A.2.4.3 Wiring diagrams

Table A-66 Wiring diagram for the CPU ST60 DC/DC/DC (6ES7288-1ST60-0AA1)

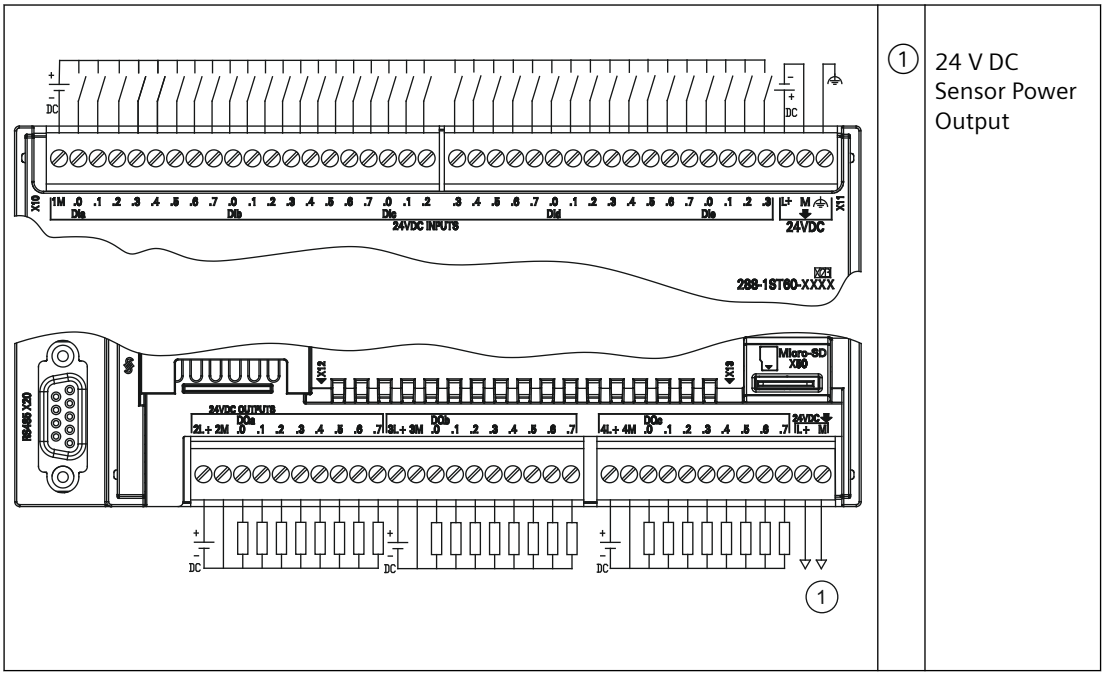


Table A-67 Connector pin locations for CPU ST60 DC/DC/DC (6ES7288-1ST60-0AA1)

Pin	X10	X11	X12	X13
1	1M	DI c.3	2L+	4L+
2	DI a.0	DI c.4	2M	4M
3	DI a.1	DI c.5	DQ a.0	DQ c.0
4	DI a.2	DI c.6	DQ a.1	DQ c.1
5	DI a.3	DI c.7	DQ a.2	DQ c.2
6	DI a.4	DI d.0	DQ a.3	DQ c.3
7	DI a.5	DI d.1	DQ a.4	DQ c.4
8	DI a.6	DI d.2	DQ a.5	DQ c.5
9	DI a.7	DI d.3	DQ a.6	DQ c.6
10	DI b.0	DI d.4	DQ a.7	DQ c.7
11	DI b.1	DI d.5	3L+	L+ / 24 V DC Out
12	DI b.2	DI d.6	3M	M / 24 V DC Out
13	DI b.3	DI d.7	DQ b.0	--
14	DI b.4	DI e.0	DQ b.1	--
15	DI b.5	DI e.1	DQ b.2	--
16	DI b.6	DI e.2	DQ b.3	--
17	DI b.7	DI e.3	DQ b.4	--
18	DI c.0	L+ / 24 V DC	DQ b.5	--